

senses a current response corresponding to the data including a binary one or a binary zero, and performs an integration of two read values, the sensing device comprises an integrator circuit for sensing the current response as an integration of charge as a function of time in consecutive timing intervals and means for storing and comparing two consecutively read values of the current response, one of which is a reference value.

3. (Twice Amended) A sensing device according to claim 11, wherein the integrator circuit comprises a switch connected in parallel over the capacitor.

Please add new claims 11 as follows:

-- 11. A sensing device for reading data stored in a passive matrix memory comprising memory cells in the form of ferroelectric capacitors, wherein said sensing device senses a current response corresponding to the data including a binary one or a binary zero, and performs an integration of two read values, the sensing device comprises an integrator circuit for sensing the current response and means for storing and comparing two consecutive read values, one of which is a reference value, and the integrator circuit comprises an operational amplifier and a capacitor connected between an inverting input of the operational amplifier and the output thereof. --